Università della Svizzera italiana | Facoltà di scienze informatiche | Advanced Learning and Research Institute ALaRI

Presentations of Master Projects in Embedded Systems Design

Date: **Thursday, 16 July, 2009**
Time: **9.00 - 13.00**
Where: **Università della Svizzera italiana, Via G. Buffi, 13 – Lugano (CH)**

Presentation room: Auditorium – main university building, third level

Admittance is free

Workshop Program

9.00 **Welcome and Opening**
Prof. Martinoli, Università della Svizzera italiana President
Prof. Luigi Dadda, ALaRI President, and Prof. Mariagiovanna Sami, ALaRI Scientific Director, Università della Svizzera italiana

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9.30 **Keynote Speech**
"Cross -cultural management"
Maria Grazia Filippini, *Vice President,* Sun Microsystems Italia SpA

Master of Advanced Studies: research project's presentation

10.00 ***Guiding theme: Security***
Side Channel attack on Escargot and stream cipher for in-car communication – *Antonio Battaglia*

Cache attacks in multiprocessor architectures – *Carlo Caione*

Security Service Negotiation Protocol for MANETs – *Pierpaolo Marcon*

Security-Aware Middleware Architecture for Self-Adaptive Systems – *Laura Micconi*

10.30 ***Guiding theme: System-On Chip***
Implementation of a system unit for managing security interaction among HW and SW components of an NoC based MPSoC system – *Stefano Carucci*

Hardware assisted malware detection on MPSoCs – *Mauro Pisano*

10.45 ***Guiding theme: Low Power Design***
Power Model of Energy Cost of Wireless Sensor Networks: Transmission and Security - *Michele Mazzante*

10.52 ***Guiding theme: Pervasive Computing***
Measuring Vineyard Condition using Wireless Sensor Network and Prediction Algorithms – *Ivana Pavlovic*

11.00 ***Guiding theme: System Level Design***

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Implementation of a Self-adaptive Component Framework: GStreamer Case Study – *Katarina Balac*

Functional Model of Embedded Systems in Future Power Systems – *Natalia Daue*

11.15 Poster's Session - Coffee Break

Master of Sciences: research project's presentation

11.25 ***Guiding theme: HW/SW for Advanced Applications***
EPON Network Scheduler Processor– *Mohamad Alhissi, Sk. Alimur Rashid*

MAPS High Level Source Code Transformations – *Ragab Moataz*

MAPS Framework Extension: Scheduling and Mapping for Multiple KPN-Applications – *Ricardo Velasquez*

Position encoder chip for SinCos encoder – *Maria Isabel Vergara Gallego*

11.55 ***Guiding theme: System-On Chip***
Sequential Design of Experiment Technique for MPSoC Optimization – *Aleksander Brankovic and Jovana Jovic*

M**odelling of a Network-on-Chip based MPSoC** – *Paolo Pezzino*

12.10 ***Guiding theme: Low Power Design***
Development of algorithms for integration of clock gating and power gating – *Gaurang Upasani*

12.20 ***Guiding theme: System Level Design***
Semi-Automatic High-Level performance Modelling of SoC architectures – *Dick Abad Nunez*

Bridging the Gap Between MDE and HW/SW Codesign – *Luis Gabriel Murillo*

Assembling Components from Requirement Specifications: Smart Home Case Study– *Amrit Panda*

Run time MPSoC resource management for reliability – *Chetan Viraktamath*

MPSoC Programming – *Arm Yehia*

Guiding theme: Pervasive Computing

Simulation of WSN for the cold chain - *Oleksiy Kasilov*

13.00 Closing Session: Diploma Ceremony
Professors MariaGiovanna Sami and Luigi Dadda – ALaRI

Aperitif follows

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